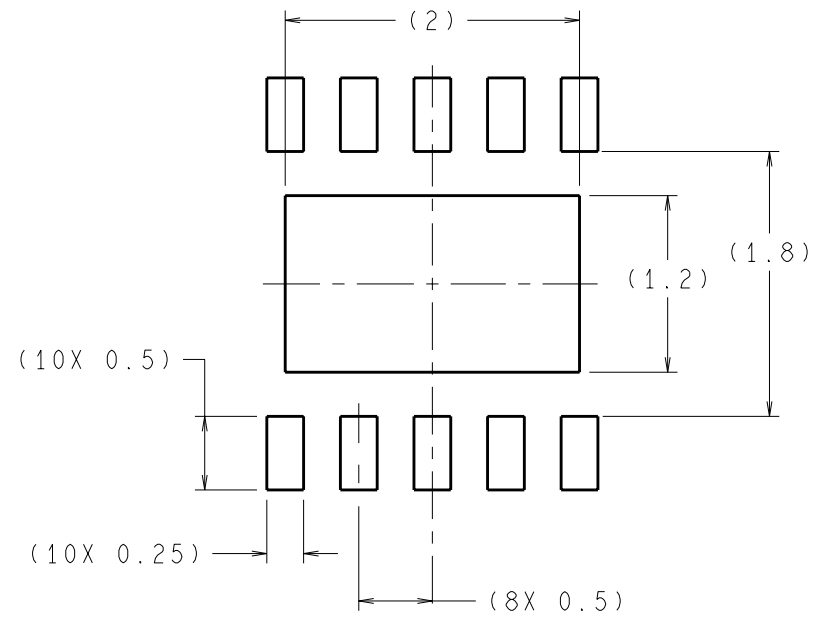
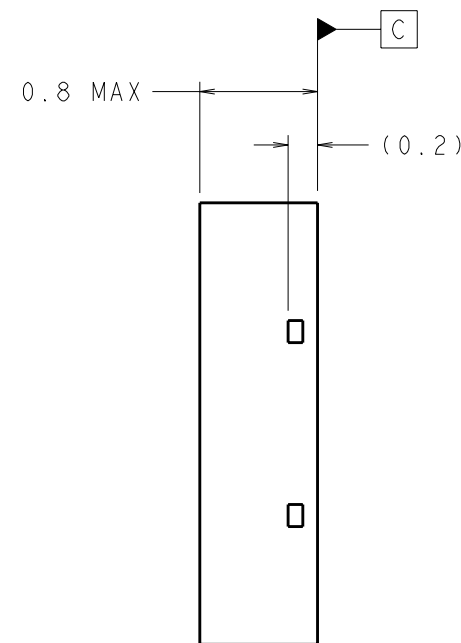
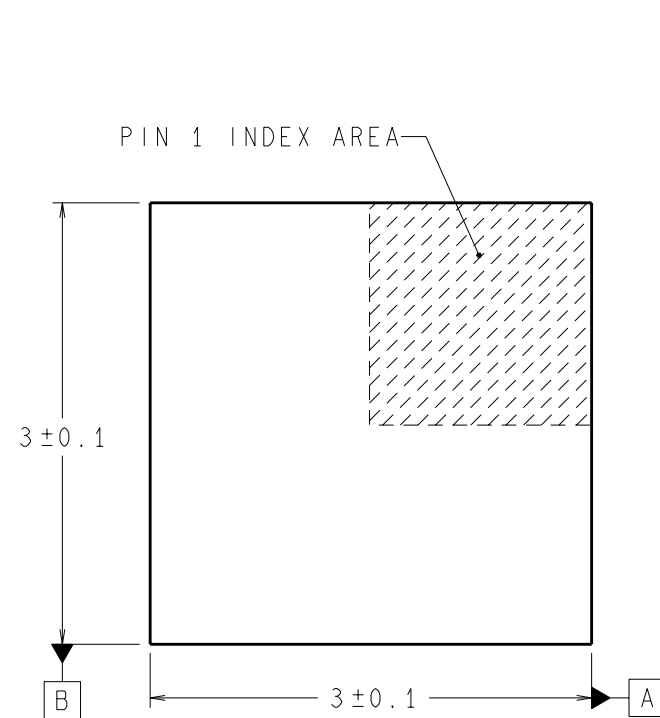


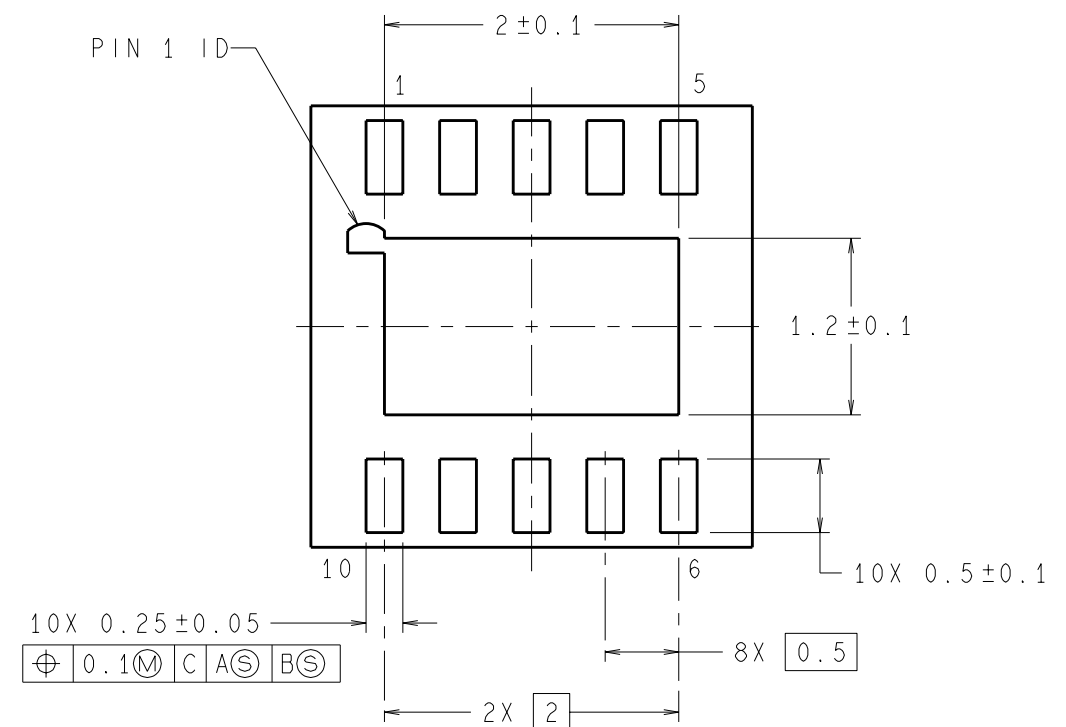
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12478	07/17/2000	SN/TL/AL



**RECOMMENDED LAND PATTERN**  
1:1 RATION WITH PKG SOLDER PADS



**DIMENSIONS ARE IN MILLIMETERS**



NOTES : UNLESS OTHERWISE SPECIFIED.

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF JULY 2000.

APPROVALS	DATE	National Semiconductor			
DRAWN N. SANTHIRAN	07/17/2000	2900 Semiconductor Dr., Santa Clara, CA 95052-8090			
DFTG. CHK. T. LEQUANG	07/17/2000	LLP, PLASTIC, DUAL, 3 X 3 X 0.75 mm BODY, 10 LD, 0.5 mm PITCH			
ENGR. CHK. A. LAW	07/17/2000				
		SCALE N/A	SIZE C	DRAWING NUMBER (SC)MKT-LDA10A	REV A
FORMERLY: N/A		SHEET 1 of 1			